

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,914,465 B2
DATED : July 5, 2005
INVENTOR(S) : Masashi Kiyose and Takuya Shiraishi

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

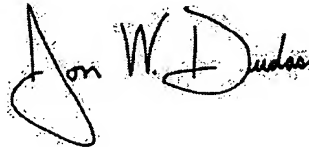
Title page, Item [54] and Column 1, line 3,

Title, please change to read:

**-- PLL CIRCUIT AND DATA RECORDING CONTROLLER EACH HAVING
DUAL PHASE-LOCKED LOOP --.**

Signed and Sealed this

Thirteenth Day of September, 2005

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a distinct "D".

JON W. DUDAS
Director of the United States Patent and Trademark Office